

Department of Electronics & Communication Engineering
School of Technology
North Eastern Hill University, Shillong- 793022

F.No.7.18/ECE/M.Tech Admission/2018

Date: 29-5-2018

M.Tech in ECE Entrance Examination (MEE-2018) 2018

The admission into the M.Tech program for **18+2** (supernumerary) seats will be done on the basis of a written test of **two hour** duration (Please refer to the syllabus below) consisting of 50 multiple choices questions (MCQs).

A merit list will be prepared based on the composite score of the following:

- (a) Marks secured in the Entrance Examination.
- (b) Weightages given to the candidates as per NEHU rules.
- (c) Weightages given to the GATE qualified candidates with valid GATE score as per NEHU rules.

The decision of the admission committee in all matters shall be binding and final.

Date, time and venue of the written test are as follows:

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|----------------------|--|
| Date of Written Test | 30 th June, 2018 (Saturday) |
| Time of written Test | 2.00 PM - 4.00 PM |
| Venue | Department of Electronics & Communication Engineering, NEHU, Umshing, Shillong-793022 (Meghalaya) |
| Reporting time | 1:15 PM at the Venue |

In case of a candidate failing to report at the reporting time and venue on the specified date and time as mentioned, he/she will forfeit his/her claim to appear for the entrance test.

Important Dates

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|---|-----------------------------|
| List of eligible Candidates. | 19 th June 2018 |
| Date of M.Tech Entrance Test | 30 th June, 2018 |
| Publication of Merit List with Admission Notification | 3 rd July 2018 |
| Date of Admission | 2 nd August 2018 |
| Display of Remaining Vacant Seats | 2 nd August 2018 |
| Date of Admission for Wait Listed Candidates | 6 th August 2018 |
| Commencement of Classes | 7 th August 2018 |

Note: The above mentioned dates are tentative.

Sd/-
Chairman
M.Tech (ECE) Admission Committee

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Detailed syllabus along with marks distributions for the entrance test of the M.Tech program:

Electronic Devices: Energy bands in silicon, intrinsic and extrinsic silicon. Carrier transport in silicon: diffusion current, drift current, mobility, and resistivity. Generation and recombination of carriers. p-n junction diode, Zener diode, tunnel diode, BJT, JFET, MOS capacitor, MOSFET, LED, PIN and avalanche photo diode, Basics of LASERs. Device technology: integrated circuits fabrication process, oxidation, diffusion, ion implantation, photolithography, n-tub, p-tub and twin-tub CMOS process. **[10 Marks]**

Networks: Network graphs: matrices associated with graphs; incidence, fundamental cut set and fundamental circuit matrices. Solution methods: nodal and mesh analysis. Network theorems: superposition, Thevenin's, Norton's and Maximum Power Transfer Theorems, Wye-Delta transformation. Steady state sinusoidal analysis using phasors. Linear constant coefficient differential equations; time domain analysis of simple RLC circuits, Solution of network equations using Laplace transform: frequency domain analysis of RLC circuits. 2-port network parameters: driving point and transfer functions. State equations for networks. **[10 Marks]**

Analog Circuits: Small Signal Equivalent circuits of diodes, BJTs, MOSFETs and analog CMOS. Simple diode circuits, clipping, clamping, rectifier. Biasing and bias stability of transistor and FET amplifiers. Amplifiers: single-and multi-stage, differential and operational, feedback, and power. Frequency response of amplifiers. Simple op-amp circuits. Filters. Sinusoidal oscillators; criterion for oscillation; single-transistor and op-amp configurations. Function generators and wave-shaping circuits, 555 Timers. Power supplies. **[12 Marks]**

Digital Circuits: Boolean algebra, minimization of Boolean functions; logic gates; digital IC families (DTL, TTL, ECL, MOS, CMOS). Combinatorial circuits: arithmetic circuits, code converters, multiplexers, decoders, PROMs and PLAs. Sequential circuits: latches and flip-flops, counters and shift-registers. Sample and hold circuits, ADCs, DACs. Semiconductor memories. **[12 Marks]**

Microprocessor (8085, 8086): Architecture, programming, memory and I/O interfacing. **[8]**

Signals and Systems: Definitions and properties of Laplace transform, continuous-time and discrete-time Fourier series, continuous-time and discrete-time Fourier Transform, DFT and FFT, z-transform. Sampling theorem. Linear Time-Invariant (LTI) Systems: definitions and

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properties; causality, stability, impulse response, convolution, poles and zeros, parallel and cascade structure, frequency response, group delay, phase delay. Signal transmission through LTI systems. [10 Marks]

Control Systems: Basic control system components; block diagrammatic description, reduction of block diagrams. Open loop and closed loop (feedback) systems and stability analysis of these systems. Signal flow graphs and their use in determining transfer functions of systems; transient and steady state analysis of LTI control systems and frequency response. Tools and techniques for LTI control system analysis: root loci, Routh-Hurwitz criterion, Bode and Nyquist plots. Control system compensators: elements of lead and lag compensation, elements of Proportional-Integral-Derivative (PID) control. State variable representation and solution of state equation of LTI control systems. [8 Marks]

Communications: Random signals and noise: probability, random variables, probability density function, autocorrelation, power spectral density. Analog communication systems: amplitude and angle modulation and demodulation systems, spectral analysis of these operations, super-heterodyne receivers; elements of hardware, realizations of analog communication systems; signal-to-noise ratio (SNR) calculations for amplitude modulation (AM) and frequency modulation (FM) for low noise conditions. Fundamentals of information theory and channel capacity theorem. Digital communication systems: pulse code modulation (PCM), differential pulse code modulation (DPCM), digital modulation schemes: amplitude, phase and frequency shift keying schemes (ASK, PSK, FSK), matched filter receivers, bandwidth consideration and probability of error calculations for these schemes. Basics of TDMA, FDMA and CDMA and GSM. [12 Marks]

Electromagnetics: Elements of vector calculus: gradient, divergence and curl; Gauss and Stokes theorems, Maxwell's equation: differential and integral forms. Wave equation. Poynting vector. Plane waves: propagation through various media; reflection and refraction; phase and group velocity; skin depth. [8 marks]

Microwave and Antenna Engineering: Transmission lines: Characteristic impedance; impedance transformation; Smith chart; impedance matching pulse excitation. Wave guides: modes in rectangular waveguides; boundary conditions; cut-off frequencies; dispersion relations, dipole antennas; antenna arrays; radiation pattern; reciprocity theorem; antenna gain. [10 Marks]